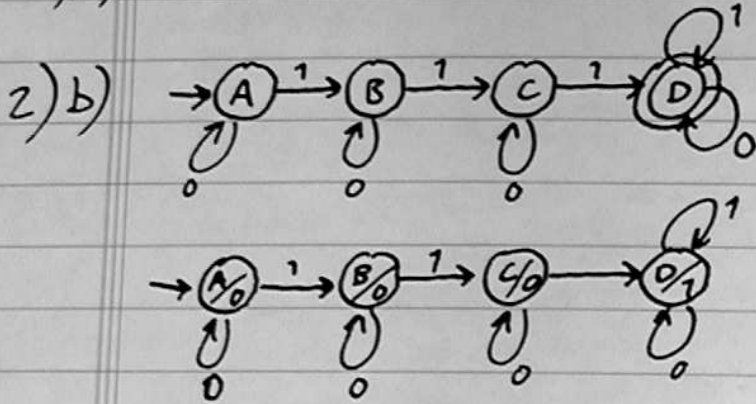


Robert Dick ECE 303 Spring 2006 final

- 1)a) Synchronous design allows correct operation even in the presence of hazards in state variable networks. Most existing CAD tools target synchronous design.
- 1)b) Higher power consumption increases temperature, resulting in decreased reliability, increased packaging and cooling cost, and increased leakage power. Higher power consumption also results in reduced battery life.
- 1)c) No. $e \propto p \cdot t$, $p \propto f$, $t \propto 1/f$
The decrease in power is offset by the increase in time to complete the task.

2)a) $0^*10^*10^*1(0+1)^*$



2)c) NS(I)

CS	0	1	Output
A ₀₀	A ₀₀	B ₀₁	0
B ₀₁	B ₀₁	C ₁₁	0
C ₁₁	C ₁₁	D ₁₀	0
D ₁₀	D ₁₀	D ₁₀	1

2)d)

B	X	X	X
C	X	X	X
D	X	X	X
	A	B	C

The machine is minimal.

2)e)

	a		
	0	1	
P ⁰	A+B		A: 00
P ¹	D+C		B: 01
			C: 11
			D: 10

2)f)

P ⁺	i
Pq	0 0
	0 1
	1 1

$P^+ = P + qi$

output_q

P	q
0 0	
1 0	

output = $P\bar{q}$

q ⁺	i
Pq	0 1
	1 1
	0 0

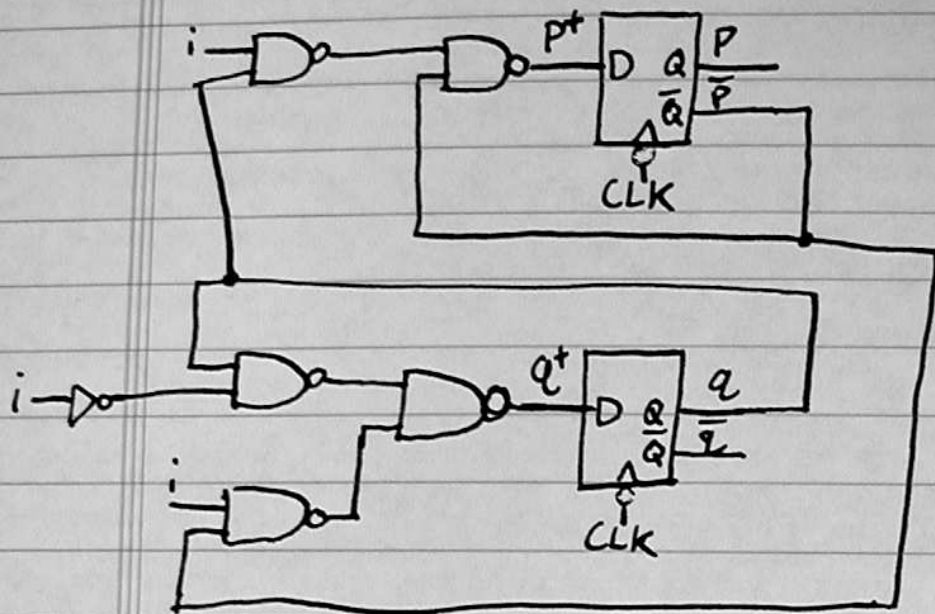
$q^+ = qi + \bar{p}i$

2)g) $P^+ = \overline{\overline{P + qi}}$
 $= \overline{\bar{P}\bar{q}\bar{i}}$

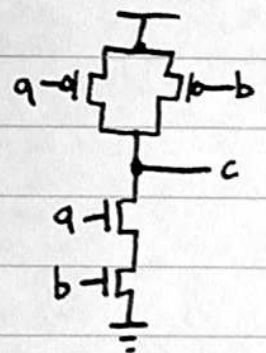
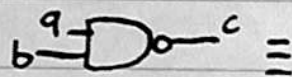
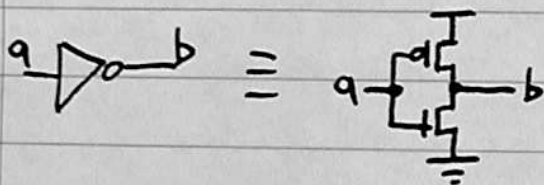
$q^+ = \overline{\overline{qi + \bar{p}i}}$
 $= \overline{\bar{q}\bar{i} \cdot \bar{p}\bar{i}}$

output = $\overline{\overline{P\bar{q}}}$
 $= \overline{\bar{P} + q}$

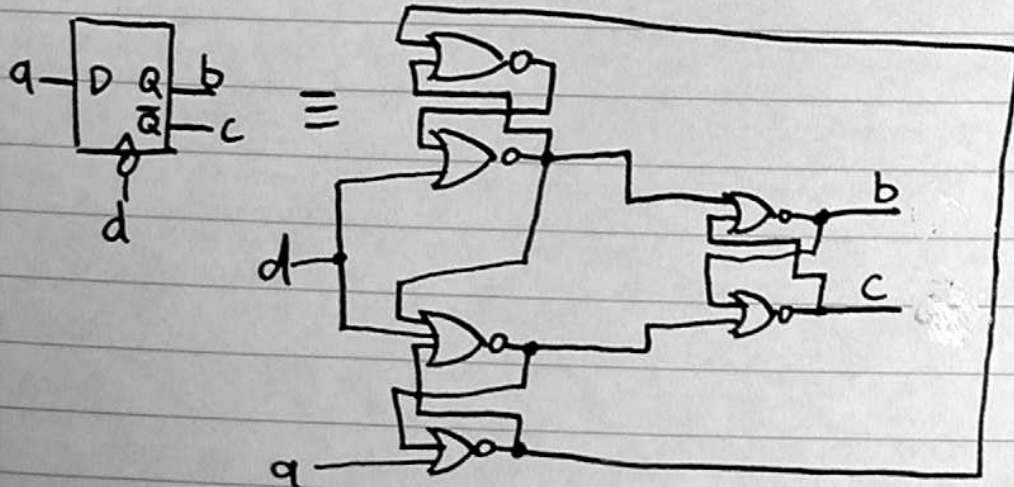
2)h)



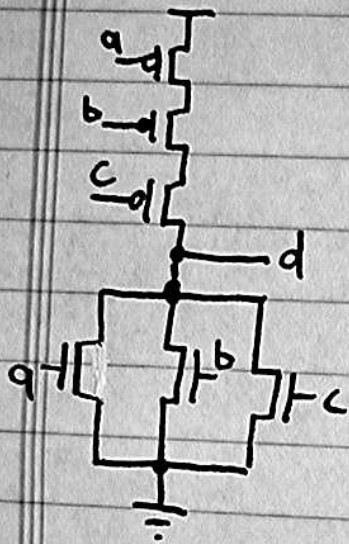
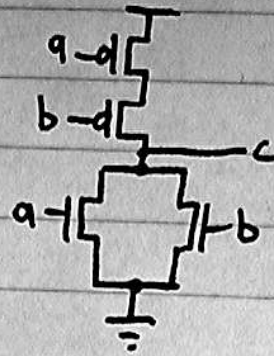
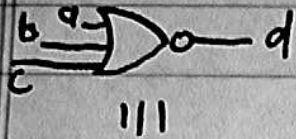
2)i)



2)j)

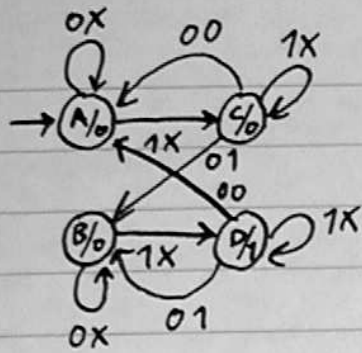


2)j) $a \rightarrow b \rightarrow c \equiv$



CLK, i

3)a)

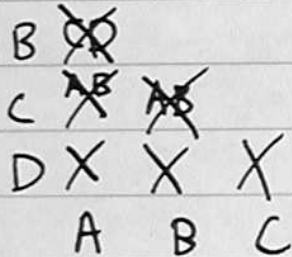


3)b)

NS(CLK, i)

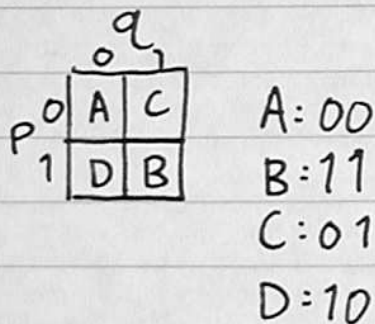
CS	00	01	10	11	Output
A ₀₀	A ₀₀	A ₀₀	C ₀₁	C ₀₁	0
B ₁₁	B ₁₁	B ₁₁	D ₁₀	D ₁₀	0
C ₀₁	A ₀₀	B ₁₁	C ₀₁	C ₀₁	0
D ₁₀	A ₀₀	B ₁₁	D ₁₀	D ₁₀	1

3)c)

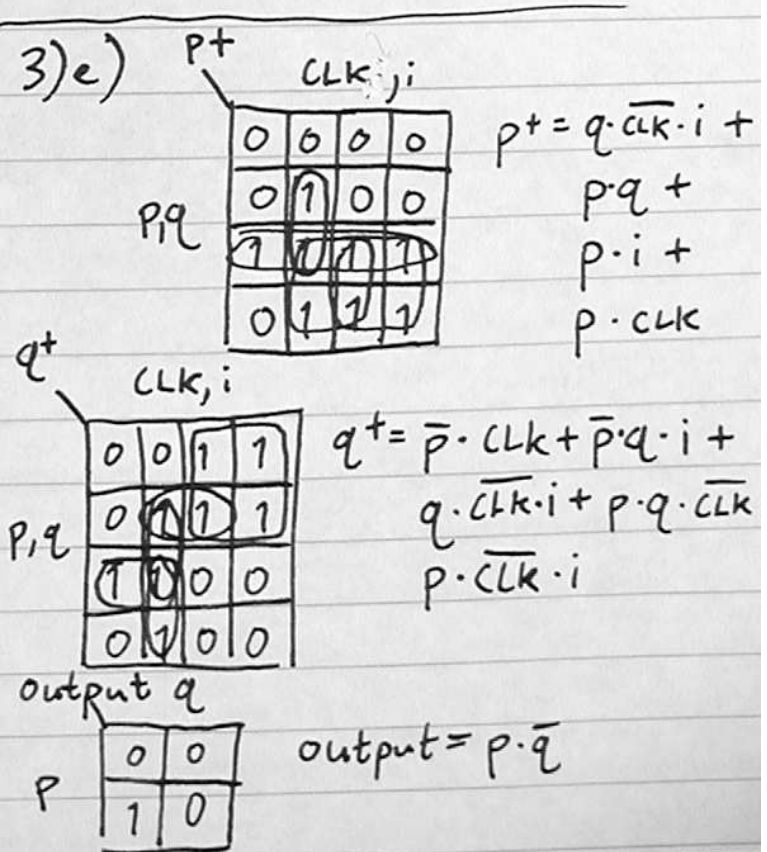


The machine is minimal.

3)d)



3)e)



4)

		cd	
ab	1	0	0
	0	0	1
	0	1	0
	0	1	1

$$f = \bar{a}\bar{b}\bar{c}\bar{d} + a\bar{c}d + \bar{a}bc + a\bar{b}c + a\bar{c}\bar{d}$$

16 literals at start

cube	kernel	gain
\bar{a}	$\bar{b}\bar{c}\bar{d} + bc$	1
a	$\bar{c}d + \bar{b}c + c\bar{d}$	2
\bar{b}	$\bar{a}\bar{c}\bar{d} + ac$	1
b	$a\bar{c}$	
\bar{c}	$\bar{a}\bar{b}\bar{d} + ad$	1
c	$\bar{a}b + a\bar{b} + a\bar{d}$	2
\bar{d}	$\bar{a}\bar{b}\bar{c} + ac$	1
d	$a\bar{c}$	
ac	$\bar{b} + \bar{d}$	2

$$\text{Use } \bar{b} + \bar{d} = k_1$$

$$f = ac k_1 + \bar{a}\bar{b}\bar{c}\bar{d} + a\bar{c}d + \bar{a}bc$$

cube	kernel	gain
\bar{a}	$\bar{b}\bar{c}\bar{d} + bc$	1
a	$ck_1 + \bar{c}d$	1
\bar{c}	$\bar{a}\bar{b}\bar{d} + ad$	1
c	$ak_1 + \bar{a}b$	1

$$\text{Use } \bar{b}\bar{c}\bar{d} + bc = k_2$$

$$f = ack_1 + \bar{a}k_2 + a\bar{c}d$$

$$4) \quad f = a c k_1 + \bar{a} k_2 + a \bar{c} d$$

cube	kernel	gain
a	$c k_1 + \bar{c} d$	1

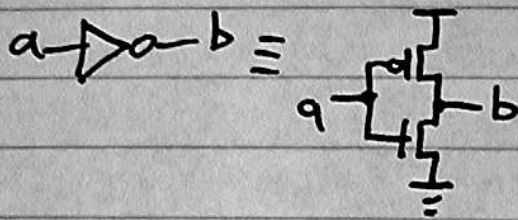
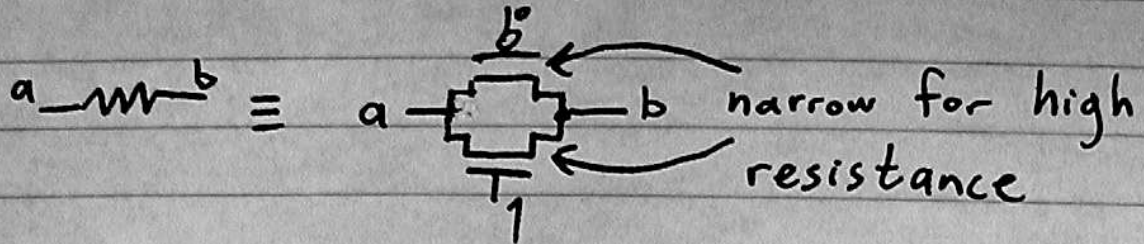
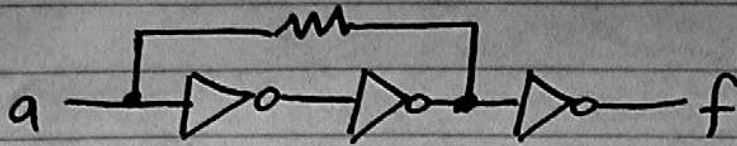
Use $c k_1 + \bar{c} d = k_3$

$$f = a k_3 + \bar{a} k_2$$

$$f = a (c (\bar{b} + \bar{d}) + \bar{c} d) + \bar{a} (\bar{b} \bar{c} \bar{d} + b c)$$

12 literals

5)



6)a) lag-flop

- 6)b)
- a = asynchronous preset
 - b = asynchronous clear
 - c = clock
 - d = input
 - e = output