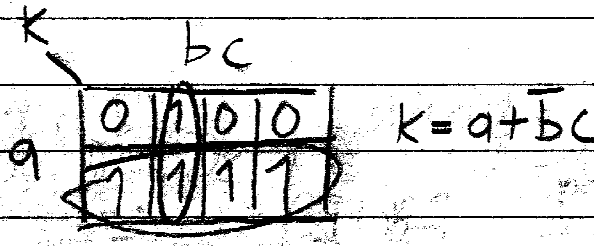
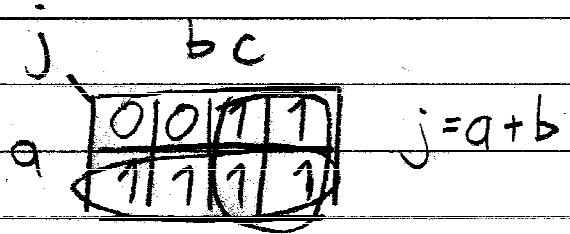


Spring '06

27 Apr 2006 EECS 303 midterm exam

1)a) The Karnaugh map method does not specify how to select a minimal subset of primes that covers all ones.

input			output	
a	b	c	j	k
0	0	0	0	0
0	0	1	0	1
0	1	0	1	0
0	1	1	1	0
1	0	0	1	1
1	0	1	1	1
1	1	0	1	1
1	1	1	1	1



2)a) The optimal solution may require more than two levels and the Quine-McCluskey method only produces two-level solutions.

2)b)	000	1	√000	√00X	⊙XX
	001	1	√001	√0X0	⊙X0
	010	1	√010	√X00	
	011	1	√100	√0X1	
	101	1	√011	√01X	
	100	X	√101	√X01	
				√10X	

		⊙XX	⊙X0
000	1	1	1
001	1	1	1
010	1		
011	1		
101	1		1

$$\begin{aligned}
 f &= \overline{a+b} \\
 &= \overline{\overline{a+b}} \\
 &= \overline{ab}
 \end{aligned}$$

~~$\frac{a+b}{b} = f$~~

2)c) *unate covering problem*

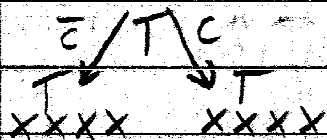
3)a) M7S can synthesize multi-level logic while Espresso is limited to two-level logic.

3)b)	XX01	1 R.E.	for XX01
	11X1	1 P.R.	11XX
	1X11	1 P.R.	unate, not T
	X01X	X	XX01 R.E.

for 11X1

XXOX

XX1X



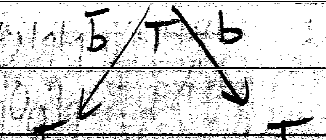
not unate,  
both T

11X1 not R.E.

for 1X11

X1XX

X0XX



not unate  
both T

1X11 not R.E.

Which R.E. covered by R.E. U.D.C.

$(\begin{matrix} XX01 \\ X01X \end{matrix})_{11X1} = XXOX, \text{ not } T, 11X1 \text{ P.R.}$

11X1 P.R.

$(\begin{matrix} XX01 \\ X01X \end{matrix})_{1X11} = X0XX \text{ not } T, 1X11 \text{ P.R.}$

3/b)

~~0001~~ Eliminate those covered by  
~~0101~~ E.E.U.D.C.: XX01, X01X  
~~1001~~

~~1011~~  
1111

11X1    1X11  
1011    1    1  
1111    1

Either fine.

$$f = \bar{c}d + abd$$

4/a)

∞    ∞  
X    X  
X    X  
X    X  
X

$$5 \cdot 2 + 3 \cdot 2 = 10 \cdot 6 = 16$$

4/b)

n levels, each of which has 1 inverter and 2 Tbs.

$$f = 2 \cdot n + 2 \sum_{i=1}^n 2^i = 2n + 2(2^{n+1} - 2) = 2n + 2^{n+2} - 4$$

$$f = 2^{n+2} + 2n - 4$$

5)

		cd		
ab	0	0	1	1
	0	0	1	1
	1	0	0	0
	0	1	0	0

$$f = \bar{a}c + ab\bar{c}\bar{d} + a\bar{b}\bar{c}d$$

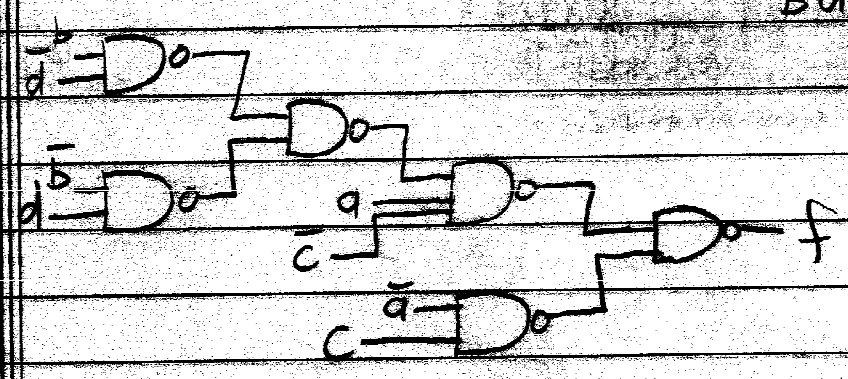
↑ ↑  
too many inputs

$$f = \bar{a}c + \bar{a}\bar{c}(b\bar{d} + \bar{b}d)$$

$$f = \overline{\bar{a}c + a\bar{c}(b\bar{d} + \bar{b}d)}$$

$$f = \overline{\bar{a}c} \cdot \overline{a\bar{c}(b\bar{d} + \bar{b}d)}$$

$$b\bar{d} + \bar{b}d = \overline{\bar{b}\bar{d} \cdot b d}$$



24 transistors

3:8  
D/C